

IN THE SPECIFICATION:

On page 8, lines 5-9, please amend the paragraph to read as follows:

The circuit buffer units of each ASIC circuit, the storage buffer unit, the first buffer unit and the second buffer unit may all be implemented as an E²PROM. An E²PROM is fixed-value buffer that may be used as read-write memory. This allows the data stored in the E²PROM to be updated or altered.

On page 17, lines 1-10, please amend the paragraph to read as follows:

The second ASIC 21 with Hall-effect device ~~21A~~ 21H is similarly configured as channel 1 described above. A buffer unit 61 is used instead of the buffer unit 60, a microprocessor unit 51 is used instead of the microprocessor unit 50, a switching unit 59 is used instead of the a switching unit 58, a signal amplifier unit 54 is used instead of the signal amplifier unit 52, a signal amplifier unit 55 is used instead of the signal amplifier unit 53. For this, the common connection possesses the index 64. A

resistor R5 is used instead of a resistor R6, and an inductance of L3 used instead of an inductance of L1.

On page 17, lines 16-19, please amend the paragraph to read as follows:

The input-switching units 55 65 and 66 are of identical design. They consist of ASIC 3 or ASIC 4, whose inputs are connected to Pin 1 and Pin 2 and Pin 5 and Pin 6. The output of ASIC 3 or ASIC 4 is fed to the input 4 of the ASIC 20 or ASIC 21.